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(54) **Induction heating cooker.**

(57) An induction heating cooker has a rectifier (13) for producing pulsating DC current, an inverter (14) which receives the pulsating current given from the rectifier (13) and produces a high frequency output current and a load circuit (15) including an output coil (21) and a resonance capacitor (22) connected in series to each other to receive high frequency output signal from the inverter, and the variation or adjustment of output electromagnetic power is made by changing duty ratio of the high frequency output current of the inverter by giving variable duty ratio setting signals to switching elements (Qa, Qb) in the inverter 14; the frequency of the high frequency output current of the inverter 14 is fixed, and therefore, unpleasant interference sound is not generated at parallel simultaneous operation.

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FIELD OF THE INVENTION AND RELATED ART STATEMENT

1. FIELD OF THE INVENTION

The present invention relates to an induction heating cooker which is to be used in homes and restaurants.

2. DESCRIPTION OF THE RELATED ART

Induction heating cookers are widely used in homes and restaurants, for its high safety and cleanness. In some high-rise building or apartment, the induction heating cooker are forcedly widely used in place of gas cookers for its high safety.

The conventional induction heating cooker comprises a rectifier for producing DC current, an inverter for oscillating a high frequency current of for instance 20KHz to 50 KHz by utilizing the DC output current of the rectifier, a load circuit which is fed with the high frequency output current from the inverter and for radiating high frequency electromagnetic wave from its output coil, and a variable frequency circuit which controls and varies the high frequency current generated by the inverter in a range from the above-mentioned frequency range of 20KHz to 50KHz.

In such conventional induction heating cooker, power of cooking is controlled by the changing of the high frequency generated in the inverter by changing the frequency issued from the variable frequency circuit. By the change of the frequency of the high frequency current from the inverter, the electromagnetic wave radiated from the coil in the load circuit widely changes and varies cooking power thereby.

When plural number of such conventional induction heating cookers are used in nearby place parallelly operated, there is a problem of unpleasant interference noise. That is, pans heated by respective induction heating cookers produce vibrations at their frequencies of the electromagnetic wave of respective induction heating cooker. And therefore, frequency difference between vibrations of neighboring plural pans makes a range of audible frequency, namely, noise which is known as interference sound is produced. Such interference sound is inevitable as far as the parallel operation of the conventional induction heating cooker is carried out changing their cooking power by wide variation of the high frequency of the inverter output current fed to the radiation coil.

OBJECT AND SUMMARY OF THE INVENTION

The present invention purports to solve the above-mentioned problem, and to eliminate the in-

terference sound even when two or more induction heating cooker are parallelly operated in nearby positions.

Furthermore, the induction heating cooker in accordance with the present invention can protect overload damages to switching elements in the inverter. Furthermore, the induction heating cooker in accordance with the present invention is free from the interference sound even after long years of service or at temperature change or at other environmental influence.

The induction heating cooker in accordance with the present invention comprises:

a rectifier for producing a DC current by rectifying an AC current from an AC power source,

an inverter having a pair of switching elements, which are each other series-connected across output terminals of said rectifier, a variable duty ratio oscillator for alternately drive the pair of switching elements in a manner that duty periods of the respective switching elements being controlled by duty-period and off-period of oscillation of the variable duty ratio oscillator, and a duty ratio setter for setting duty ratio of the oscillation, and

a load circuit having an output coil and a resonance capacitor.

According to the induction heating cooker of the Present invention, the heating power can be varied freely By changing duty ratios of the switching elements keeping the frequency of the high frequency oscillator substantially constant.

In a detailed species of the invention, drivings of the switching elements are so controlled that the duty ratio is selected in such a range of under 1/2 or above 1/2, so that a feed back control of the duty ratio to make output heating power at desired value is stable.

In a further detailed species in accordance with the present invention, a pair of inverters and a pair of load circuits are provided and respective variable duty ratio oscillators are mutually coupled for realizing synchronous oscillation of the two variable duty oscillators.

While the novel features of the invention are set forth particularly in the appended claims, the invention, both as to organization and content, will be better understood and appreciated, along with other objects and features thereof, from the following detailed description taken in conjunction with the drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG.1 is a circuit diagram of a preferred embodiment of induction heating cooker embodying the present invention.

FIG.2 is a waveform chart showing operation of the induction heating cooker of FIG.1.

FIG.3 is a characteristic graph showing operation of the circuit of FIG.1.

FIG.4 is a circuit diagram of another preferred embodiment of induction heating cooker in accordance with the present invention.

FIG.5 is a circuit diagram of still another preferred embodiment of induction heating cooker in accordance with the present invention.

FIG.6 is a circuit diagram of still another preferred embodiment of induction heating cooker in accordance with the present invention.

FIG.7(A) is a circuit diagram of one preferred example of a variable duty ratio oscillator and a duty ratio setter.

FIG.7(B) is a circuit diagram of a preferred embodiment of the variable duty ratio oscillator and limiter circuit.

FIG.8 is a waveform chart showing operation of the circuits of FIG.7(A) and FIG.7(B).

FIG.9 is a circuit diagram of still another preferred embodiment of induction heating cooker in accordance with the present invention.

FIG.10 is a circuit diagram of a preferred embodiment of variable duty ratio oscillators of the embodiment of FIG.9.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

FIG.1 is a circuit diagram of a first preferred embodiment of the induction heating cooker in accordance with the present invention.

As shown in FIG.1, the induction heating cooker has a rectifier 13, an inverter 14 and a load 15. The rectifier 13 receives AC current from an AC power source 12, and the AC current is rectified by a known diode circuit 16 and smoothed by a smoothing circuit including a smoothing choke 17 and a smoothing capacitor 18, and issues rectified output to the input terminals V_4 and V_5 of the inverter 14. The inverter 14 includes a pair of switching elements Qa and Qb which is to be alternately driven (switched on) by receiving respective driving signal "a" and "b" from a variable duty ratio oscillator 19. A duty ratio setter 20 is for setting desired duty ratio for outputting desired induction heating power to a load circuit 15. By receiving the output "c" of the duty ratio setter 20, the variable duty ratio oscillator 19, issues two output signals "a" and "b" to the switching elements Qa and Qb, respectively, so as to control duty ratios of the high frequency current of a substantially constant period to be controlled by the switching elements Qa and Qb. The load circuit 15 includes an output coil 21, which is for radiating electromagnetic wave for induction-heating of a cooking object (not shown), and a resonance capacitor 22, which is connected in series to the

output coil 21. A series connection of the output coil 21 and the resonance capacitor 22 constitutes a series resonance circuit. The load circuit 15 is connected to receive output high frequency current from the inverter 14. In this embodiment, the load circuit 15 is connected across output terminals V_2 , which is a junction point between the two switching elements Qa and Qb, and V_3 , which is the low potential point of the inverter 14.

FIG.2 is a waveform chart showing output voltage V_0 to be fed to the load circuit 15 from the output terminals V_2 and V_3 . The output signal V_0 is a square wave, as shown in FIG.2, and its amplitude is substantially identical with the voltage E of the output voltage of the rectifier 13.

The variable duty ratio oscillator 19 varies duty ratio $D = T_a / (T_a + T_b)$ responding to instruction given by the duty ratio setter 20. And the oscillation frequency of the variable duty ratio oscillator 19 is selected substantially around the resonance frequency of the series resonant circuit consisting of the output coil 21 and resonance capacitor 22 of the load circuit 15. Now by defining

$$D = T_a / (T_a + T_b) \quad (1),$$

the amplitude of the fundamental wave V_{01} of the output voltage V_0 across the load circuit 15 is given by Fourier series' expansion as follows:

$$V_{01} = 2E \sin(\pi D) / \pi \quad (2).$$

The DC component of the voltage V_0 does not give an influence to the load circuit 15 which is the series resonance circuit having the capacitor 22. And higher harmonic components have smaller amplitudes V_{0n} , as order number becomes higher as is represented by the equation 3, and further the frequency of the higher harmonics component is sufficiently higher than the resonance frequency of the load circuit 15. Therefore, the harmonics components have no influence on the load circuit 15.

Thus, the operation of the circuit of FIG.1 can be, in approximation, considered by providing that a high frequency sinusoidal wave voltage having amplitude of V_{01} is impressed on the load circuit 15. Then, as apparent from the equation (2), the changing of the duty ratio D can change, the voltage, hence output electromagnetic wave power. FIG.3 shows relation between the duty ratio D and the output power. That is, by changing the duty ratio D through instruction from the duty ratio setter 20, the output heating power can be changed without changing the oscillation frequency.

According to this embodiment, by varying duty ratio D, power of the electromagnetic heating output can be widely changed without changing the

frequency of the electromagnetic wave radiated from the output coil 21. Therefore, even though two or more induction heating cookers are used simultaneously in nearby places, generation of unpleasant interference sound or noise can be avoided.

Apart from the conventional induction heating cooker wherein rectifier is designed to output almost completely smoothed DC output therefrom, the rectifier circuit 13 in the present invention is designed such that the output current from the rectifier 13 to be given to the inverter 14 has a waveform of full-wave rectified current by selecting the electric constants of the smoothing choke 17 and the filter capacitor 18. By designing so, only using smaller smoothing choke and smoothing capacitor than the conventional circuit, a sufficient smoothing can be made, and furthermore higher harmonic waves in the input current to the inverter 14 are eliminated. When the time constant determined by the inductance of the smoothing choke 17 and capacitance of the smoothing capacitor 18 is too small, the input current to the inverter has zero value periods between each half circle peaks, and hence, contains much higher harmonic components, thereby resulting in noisy higher harmonic vibration sound of the pan. On the contrary, when the time constant is too large, higher harmonic distortion in the input current becomes large and higher harmonic components which is taken in from the AC power source increases, and furthermore, size of the induction heating cooker increases uselessly.

Furthermore, apart from the conventional induction heating cooker wherein switching of the apparatus is carried out at arbitrary instant thereby sometimes producing a surge voltage due to cutting off of large current at the timing near peak value of AC current, the induction heating cooker in accordance with the present invention can avoid such generation of harmful surge voltage to the AC power line by devising to switch off the large current of the apparatus at the instant of zero-cross point of AC current. Thereby, undesirable effect by the surge pulse to nearby electric apparatuses can be prevented.

FIG.4 shows another embodiment of induction heating cooker in accordance with the present invention. Corresponding parts and components to the first embodiment are shown by the same numerals and marks, and the description thereon made in the first embodiment similarly apply. Differences and features of this embodiment from the first embodiment are as follows. Principle feature of this embodiment is connection of one end of the output coil 21 in the load circuit 15 to the lower output end V_3 of the inverter 14. The circuit configuration has an advantage of low radiation of

noise from the output coil 21 because the output coil is connected to substantially grounded with respect to high frequency component.

FIG.5 is a circuit diagram of another embodiment of the present invention. Corresponding parts and components to the first embodiment are shown by the same numerals and marks, and the description thereon made in the first embodiment similarly apply. Differences and features of this embodiment from the first embodiment are as follows. In the circuit configuration of this embodiment, the load circuit 15 is connected between the high potential output terminal V_1 and the medium potential output terminal V_2 of the inverter. This embodiment has a technical advantage in stable operation of the switching element Q_a , which has floated driving signal source and hence has a possibility of mal-operational conduction at the switching ON of the AC power source 12.

FIG.6 is a circuit diagram of another embodiment of the present invention. Corresponding parts and components to the first embodiment are shown by the same numerals and marks, and the description thereon made in the first embodiment similarly apply. Differences and features of this embodiment from the first embodiment are as follows. Circuit configuration of this embodiment is that two capacitors 23 and 24 are connected in series to each other across the output terminal V_1 and V_3 of the inverter 14, and the output coil is connected between the medium output terminal V_2 and the junction point between the two capacitors 23 and 24. According to this circuit configuration, the switching elements Q_a and Q_b of a relatively low break down voltage can be used, because possible high voltage produced across the output terminals V_1 and V_3 transiently at switching ON of the AC power source 12 are divided into two voltages by the series connected resonance capacitors 23 and 24.

FIG.7(A) is a circuit diagram of one example showing detailed circuit configurations of the variable duty ratio oscillator 19 and the duty setter 20. The duty ratio setter 20 has a reference voltage source 25 and an input current detector 26 which issues an output voltage proportional to the input AC current from the AC power source 12. The input current detector is configured, for instance, by a known current transformer (not shown) coupled to the input line between the AC power source 12 and the rectifier 13 and a rectify-smoothing circuit. As is clearly understandable, the input AC current has high correlation with the output electromagnetic power from the output coil 21 of the load circuit 15. And therefore, by detecting the input current amount and using the measured current amount as input current for a feed back loop to control the inverter 14, the output electromag-

netic power from the output coil 21 can be stably controlled. A differential amplifier 27 receives voltage from the reference voltage source 25 and the output from the input current detector 26 and issues an output "c", which is amplified difference between the two inputs to the differential amplifier 27, and gives it to the variable duty ratio oscillator 19. The output "c" is given through a resistor 28 to an input terminal "f" of a voltage comparator 29, and the same signal is further given to a variable range limiter 37, which is consisting of a transistor 30 and a voltage divider having two resistors 31 and 32. The signal at the input terminal "f" of the comparator 29 is limited of its voltage by the variable range limiter 37 under such a voltage level VL of $VL = V_{32} + V_{BE30}$, wherein V_{32} is a divided voltage at the junction point between the registers 31 and 32 and given to the base of the transistor 30, and V_{BE30} is a base-emitter voltage of the transistor 30. The variable duty ratio oscillator 19 comprises, besides the variable range limiter, a constant current source 33, a capacitor 34 to be charged by a current from the constant current source 33, a transistor 35 for short-circuiting the capacitor 34, a fixed period pulse generator 36 and a distribution circuit 38. An output pulse from the fixed period pulse generator 36 is shown in a waveform "d" in FIG.8.

The operation of the circuit of FIG.7(A) is as follows. Voltage to the input terminal "e" of the voltage comparator 29 is charged by the current from the constant current source 33 and is charged linearly as shown in curve "e" of FIG.8. When output pulse "d" is given from the fixed period pulse generator 36, the transistor 35 short-circuits the capacitor 34. Therefore, the voltage of the input terminal "e" becomes a saw-tooth wave of a constant period as shown by the wave form "e" of FIG.8. Therefore, the output of the voltage comparator 29 becomes a pulse wave as shown by a wave form "g" in FIG.8, which is high in the period of $e < f$. The distribution circuit 38 outputs low level (Lo) at its output terminal "a" and high level (Hi) at its output terminal "b" when the output signal "g" of the comparator 29 is high (Hi). On the contrary, when the output "g" is low (Lo) the output "a" of the distribution circuit becomes high (Hi) and output "b" thereof becomes low (Lo). As is well known, in order to prevent undesirable parallel switching-ON of the switching elements Qa and Qb of FIG.1, it is preferable to provide a suitable short low-level period (dead time) between a fall and subsequent rise timings of pulses "a" and "b". When the upper limit VL of the signal of the input terminal "f" is determined to be $(1/2)E$ or smaller, the duty ratio D of equation 1 is limited in the range of $1/2$ or larger of FIG.3, as is obvious from the waveforms "a" and "b" of FIG.8. But for the

variable range limiter 37 of FIG.7(A), if the output power of the inverter 14 is intended to be controlled to P_1 of FIG.3, there are two corresponding points D_1 and D_2 on the curve of FIG.3, thereby inducing a difficulty of stably carrying out a feedback control using the output of the input current detector 26. Therefore, the variable range limiter 37 provide a stable feedback control of the output electromagnetic wave power by the limiting of the operation range to R_2 of FIG.3, thereby making uniquely determining the duty ratio corresponding to the output power P_1 .

As is easily understandable, when polarities of the input terminals of the voltage comparator 29 and the differential amplifier 27 of FIG.7a are inverted, the operation range becomes in the range of R_1 of FIG.3, wherein $D < 1/2$, and also in this case a stable operation is obtainable.

As a modified embodiment of FIG.7(A), in place of the input AC current, other detected values such as current of the output coil 21, voltage of the resonance capacitor 22, and the like physical amounts, which have correlation with the output electronic magnetic power, may be used as input signal to be given to the differential amplifier 27.

Furthermore, overloads to the switching elements Qa and Qb owing to variation of load, etc., can be avoided by detecting a physical amount which has correlation with the output currents of the switching element Qa and Qb and using it in a feedback control system. For such physical amount, for instance, peak current value of the switching elements Qa and/or Qb, peak voltage value of the resonance capacitor 22, peak current value of the output coil 21, etc. can be used.

FIG.7(B) is a circuit diagram of a preferred embodiment of the variable duty ratio oscillator 19 comprising the distribution circuit 38. The distribution circuit 38 comprises a pair of transistors 40 and 40' which are coupled with a pre-stage transistors 42, 42'. Base driving lines h and h' give base current to the transistors 42 and 42' only when a sufficient power is supplied to the variable duty ratio oscillator 19 from the AC power source 12. When the AC power source 12 is not connected to the circuit of the induction heating cooker, the variable duty ratio oscillator 19 is not supplied with the sufficient power current, and therefore, the transistor 42 and 42' are cut off. And therefore, clipping circuits consisting of the transistors 40 and 40' and registers 41 and 41' limits the gate voltages of the switching elements Qa and Qb around below V_{BE40} and $V_{BE40'}$ (base-emitter voltages of the transistors 40 and 40'), respectively. By this clipping action of the driving signals "a" and "b", maloperational conductions of the switching elements Qa or Qb (through their feedback capacitance) is prevented even when such maloperation is about to be in-

duced by generation of sharp rise of collector-emitter voltages of the transistors Qa and Qb at the instant of connection of the AC power source to the induction heating cooker.

FIG.9 is a circuit diagram of another embodiment of the present invention. Corresponding parts and components to the first embodiment are shown by the same numerals and marks, and the description thereon made in the first embodiment similarly apply. Differences and features of this embodiment from the first embodiment are as follows.

In this embodiment, a common AC power source 12 supplies AC current to a first rectifier 13A and a second rectifier 13B. A first inverter 14A and a second inverter 14B receive rectified DC current from the first rectifier 13A and the second rectifier 13B, respectively. A first load circuit 15A and a second load circuit 15B receive high frequency power from the first inverter 14A and the second inverter 14B, respectively. The first rectifier 13A and the second rectifier 13B comprises a first diode bridge 16A and a second diode bridge 16B, a first smoothing choke 17A and a second smoothing choke 17B, and a first smoothing capacitor 18A and a second smoothing capacitor 18B, respectively. The first inverter 14A comprises a first switching element QaA and a second switching element ObA, and the second inverter 14B comprises a third switching element QaB and a fourth switching element QbB, respectively. Further, the first inverter 14A further comprises a first variable duty ratio oscillator 19'A and a first duty ratio setter 20A. The second inverter similarly comprises a second variable duty ratio oscillator 19'B and a second duty ratio setter 20B. A first load circuit 15A and a second load circuit 15B comprise a series connection of a first output coil 21A and a first resonance capacitor 22A and a second series connection of a second output coil 21B and a second resonance capacitor 22B, respectively. The detailed configurations of the circuit and their operations are similar to those of FIG.1. And on features different from the first embodiment, detailed description is made with reference to FIG.10. The above-mentioned configurations a variable duty ratio oscillators 19'A and 19'B are each other commonly connected by a line h. As shown in FIG.10, both variable duty ratio oscillators 19'A and 19'B shares a common fixed period pulse generator 36. That is output of the common fixed period pulse generator 36 fed output to the bases dA and dB of the transistors 35A and 35B of the variable duty ratio oscillators 19'A and 19'B, respectively. Therefore, as will be easily understood from the aforementioned descriptions made for the circuit of FIG.7(A), both variable duty ratio oscillators 19'A and 19'B oscillates in synchronism with each other. By the configuration shown by FIG.9 and FIG.10,

even there may be some slight difference between the high frequency of the output current from two inverters 14A and 14B owing to divergence of mass production, temperature difference, time lapse change of characteristics of electronics components therein, or the like, there is no interference sound produced when the two induction heating cooker are operated simultaneously. This is because the two output coils 21A and 21B output electromagnetic wave of accurately synchronized operation.

Of course, the configuration of plural output coil to be parallelly operated in a synchronized manner may be configured to provide three or more output coils.

Although the invention has been described in its preferred form with a certain degree of particularity, it is understood that the present disclosure of the preferred form has been changed in the details of construction and the combination and arrangement of parts may be resorted to without departing from the spirit and the scope of the invention as hereinafter claimed.

Claims

1. An induction heating cooker comprising:

a rectifier for producing a DC current by rectifying an AC current,

an inverter having a pair of switching elements, which are each other series-connected across output terminals of said rectifier, a variable duty ratio oscillator for alternately drive said pair of switching elements in a manner that duty periods of said respective switching elements being controlled by duty-period and off-period of oscillation of said variable duty ratio oscillator, and a duty ratio setter for setting duty ratio of said oscillation, and

a load circuit having an output coil and a resonance capacitor.

2. An induction heating cooker in accordance with claim 1, wherein

said load circuit is connected to a low-potential side output terminal of said inverter.

3. An induction heating cooker in accordance with claim 1, wherein

said load circuit is connected to a high-potential side output terminal of said inverter.

3. An induction heating cooker in accordance with claim 1, wherein

said load circuit has two resonance capacitors which are connected in series to each other across high-potential side and low-potential sides output terminals of the inverter and said output coil is connected between junction point between said two capacitors and intermediate output terminal of said inverter.

5. An induction heating cooker in accordance with

claim 1, wherein

said duty ratio setter comprises a physical amount detector for detecting physical amount which has a correlation with input power with said induction heating cooker or with current of said first and second switching elements and a differential amplifier which compares output of said physical amount detector with a predetermined reference value, and

said variable duty ratio oscillator comprises a variable range limiter for limiting duty ratio of said variable duty ratio oscillator in either range of smaller than 1/2 or larger than 1/2 of the duty ratio.

6. An induction heating cooker in accordance with claim 1 which comprises:

two sets of said inverters and

two sets of said load circuits, in which variable duty oscillators oscillate in synchronism with each other.

7. An induction heating cooker in accordance with claim 1, wherein

said rectifier comprises an inductor and a capacitor, time constant given by an inductance of said inductor and a capacitance of said capacitor is selected in a manner that waveform of output current of said rectifier is of a full-wave rectified wave.

8. An induction heating cooker in accordance with claim 1 which has

means to stop oscillation of said variable duty ratio oscillator substantially in synchronism with zero-cross timing of said AC current.

9. An induction heating cooker in accordance with claim 1, wherein

said variable duty oscillator retains potential at said pair of switching elements substantially zero, when said induction heating cooker is disconnected from said AC power source.

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FIG. 1

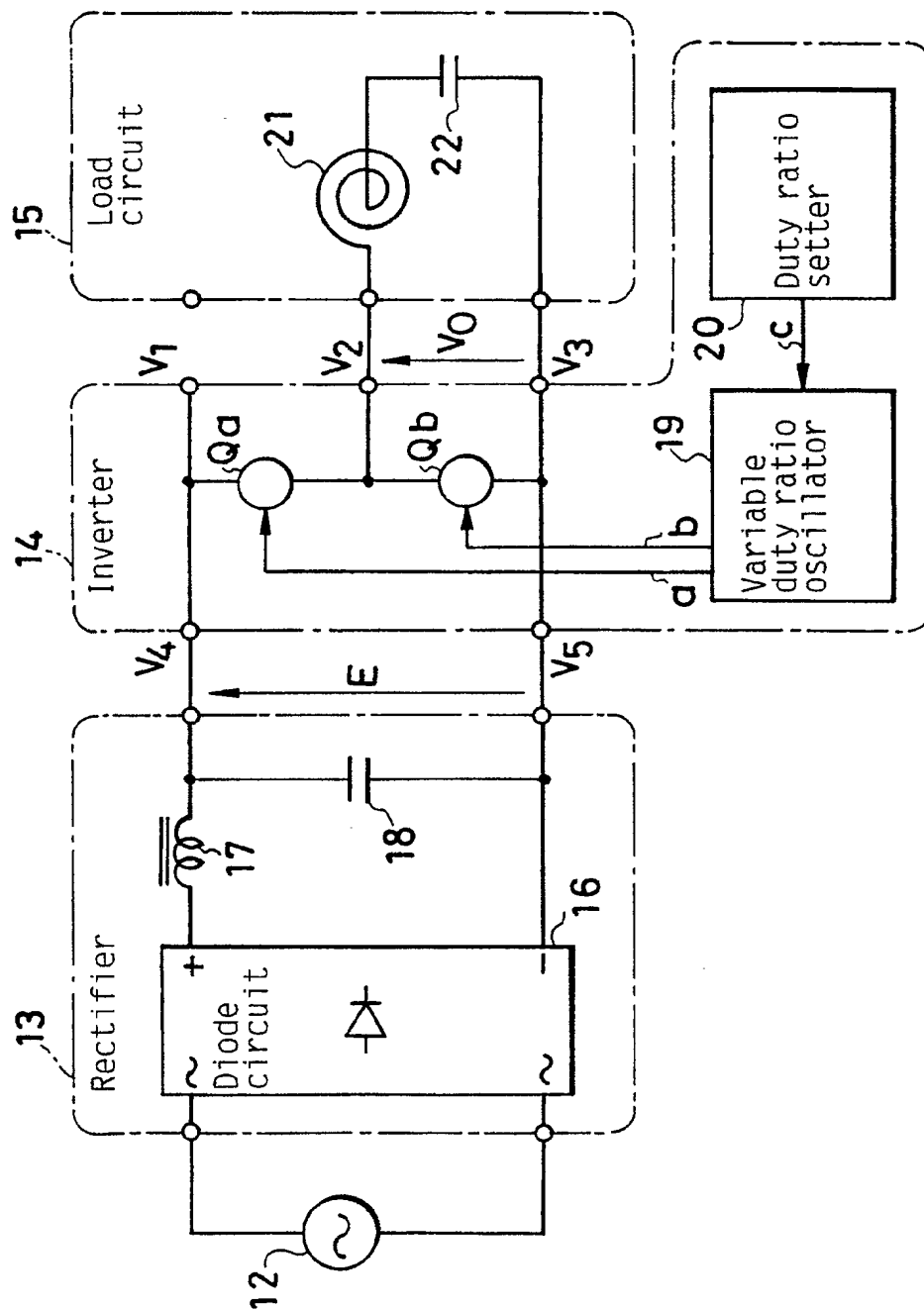


FIG. 2

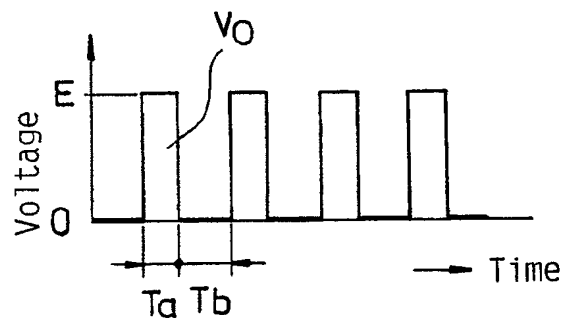


FIG. 3

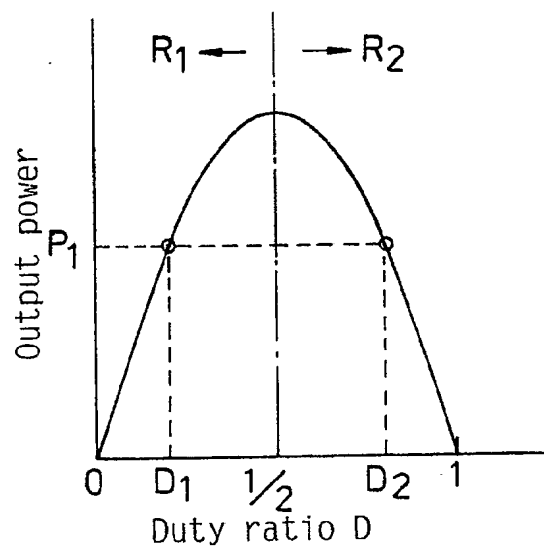


FIG. 4

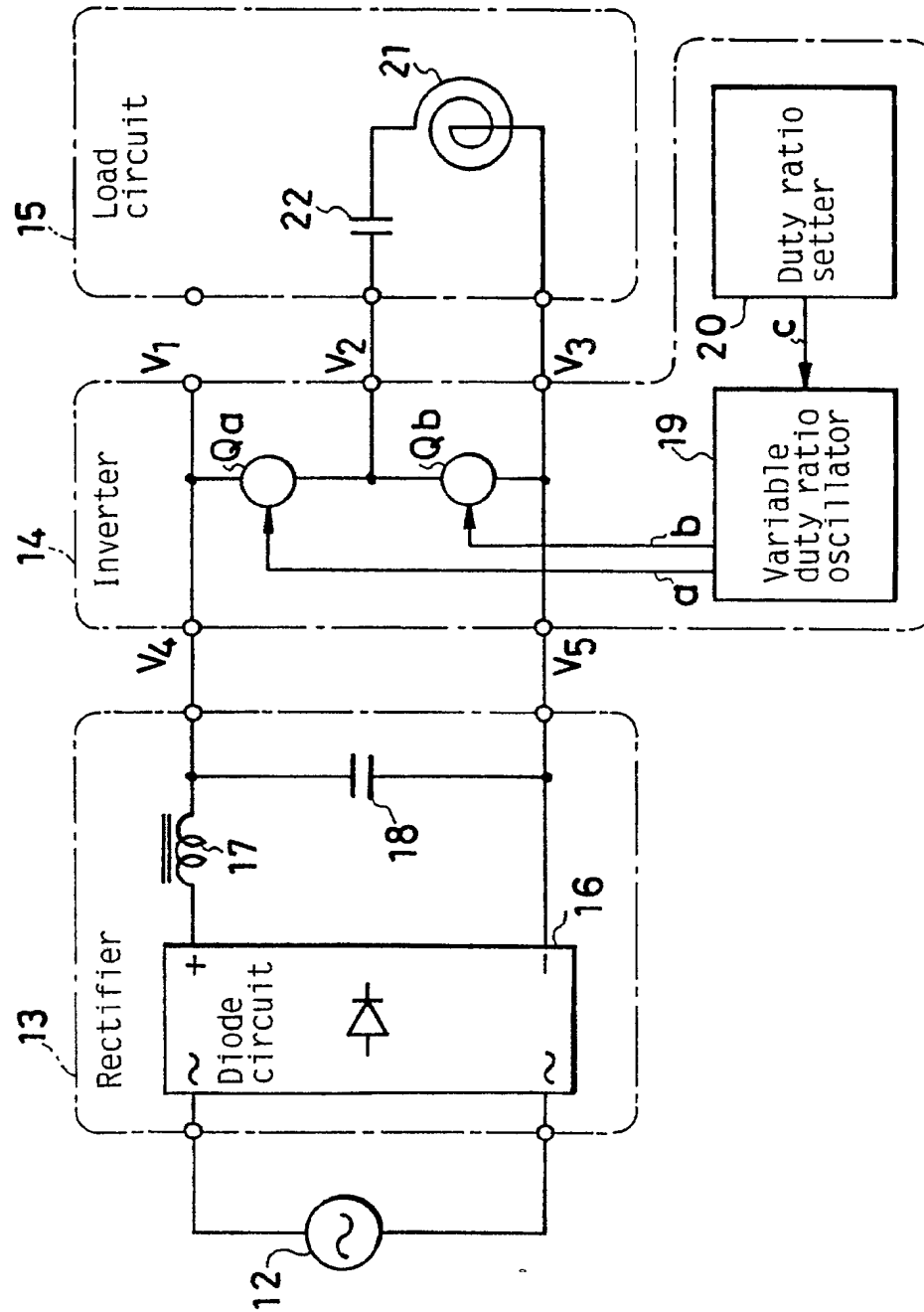


FIG. 5

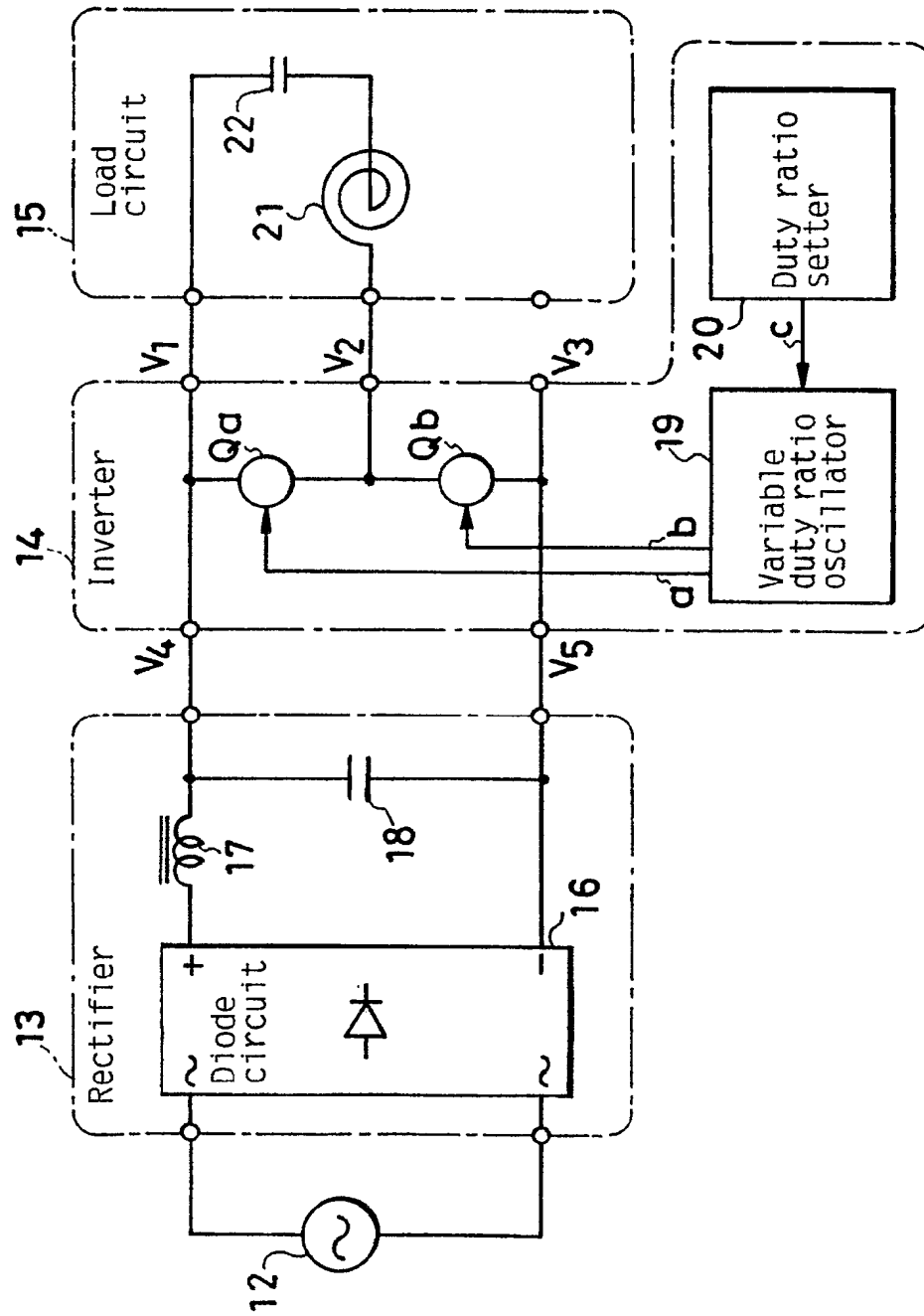


FIG. 6

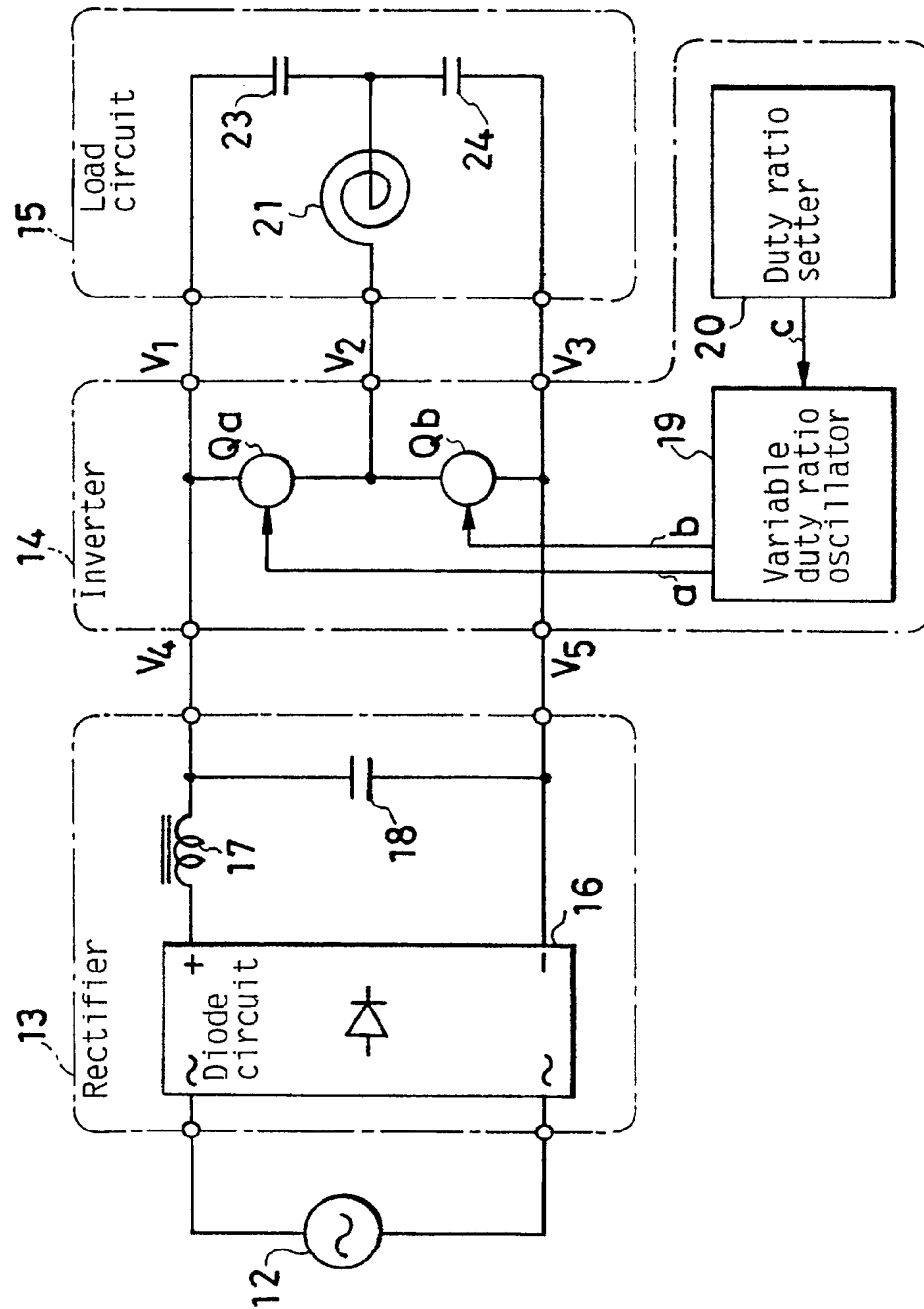


FIG. 7 (A)

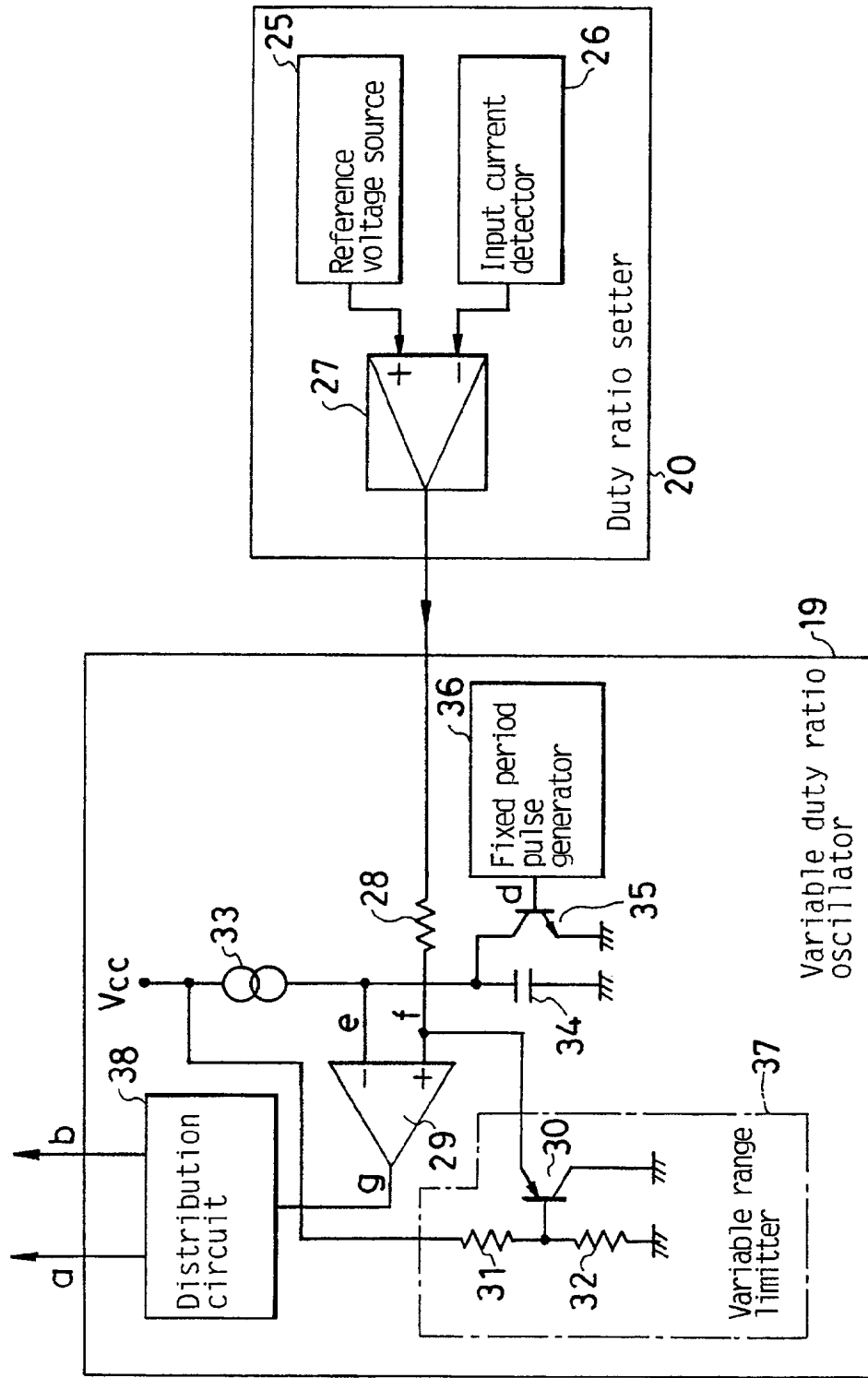


FIG. 7 (B)

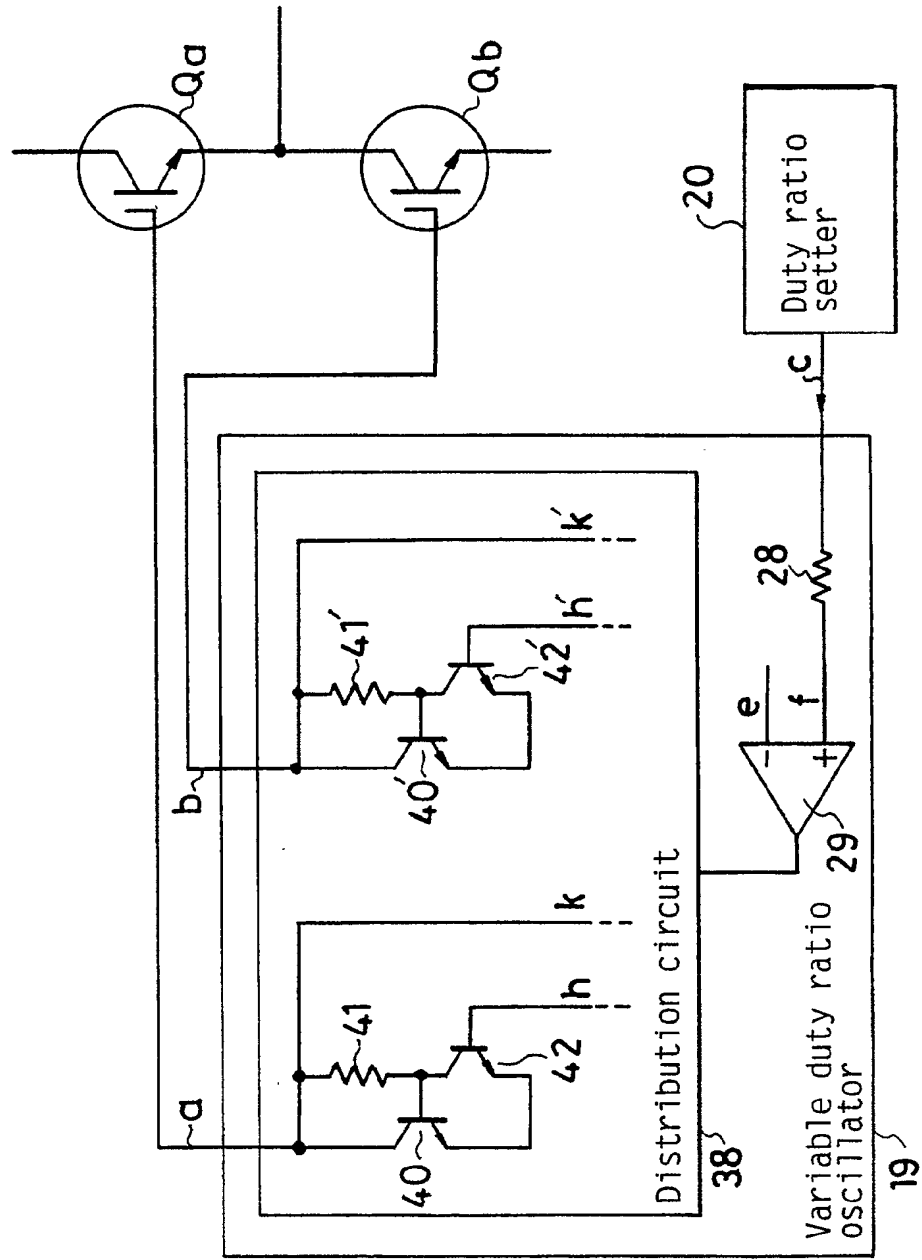


FIG. 8

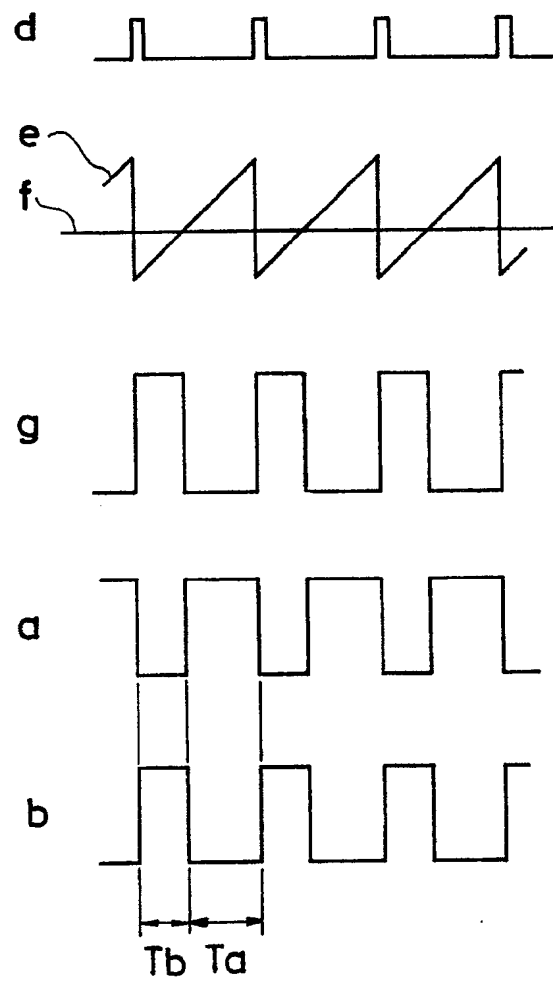


FIG. 9

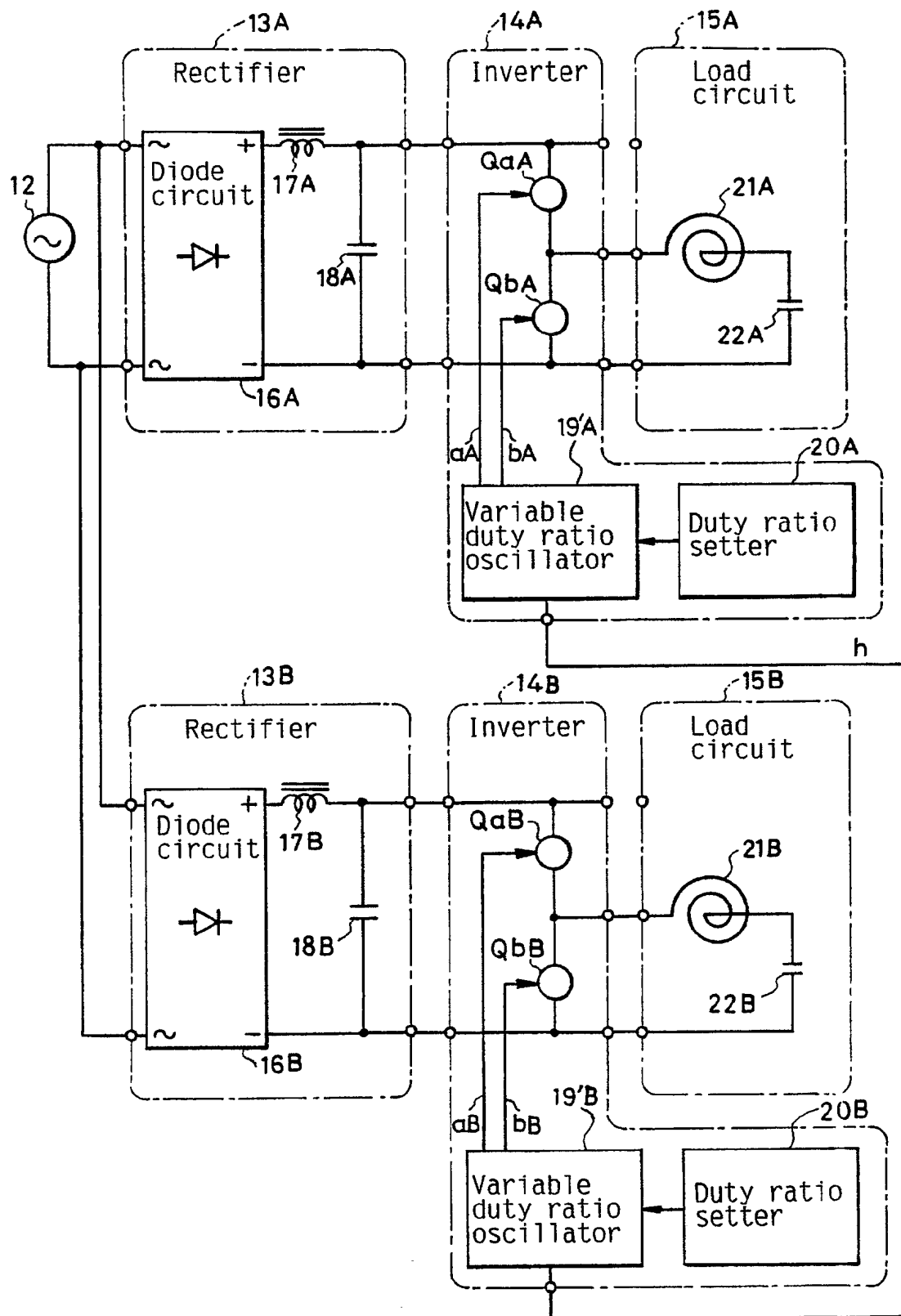
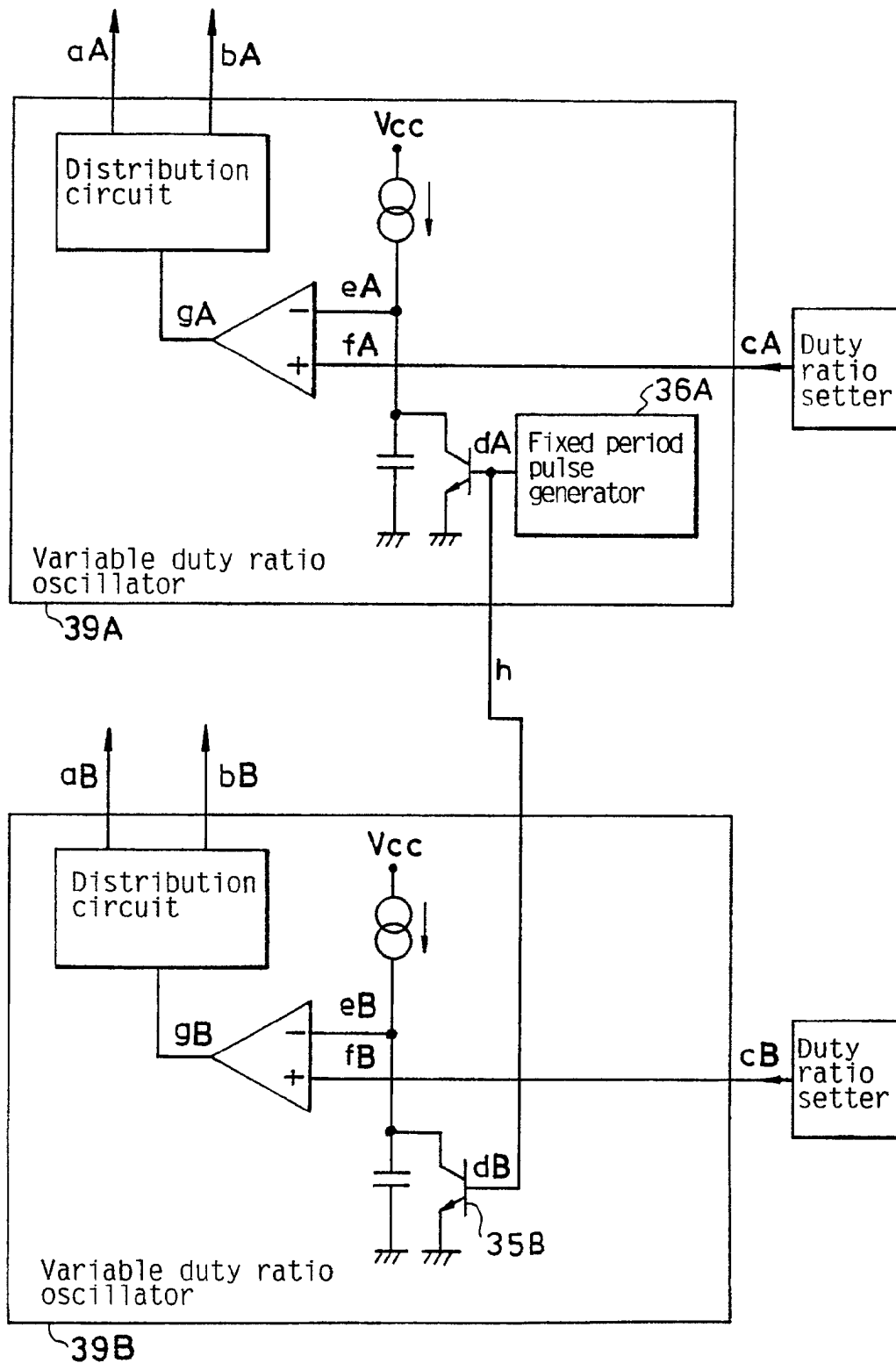


FIG.10



Induction heating cooker.**Publication number:** EP0460279 (A2)**Publication date:** 1991-12-11**Inventor(s):** OMORI HIDEKI [JP]; NOMA HIROFUMI [JP]; KOMINAMI HIDEYUKI [JP]**Applicant(s):** MATSUSHITA ELECTRIC IND CO LTD [JP]**Classification:****- international:** **H05B6/12; H05B6/06; H05B6/12; H02M7/538; H05B6/06;**
(IPC1-7); H02M5/00; H05B6/06**- European:** H02M7/538C; H05B6/06C**Application number:** EP19900116562 19900829**Priority number(s):** JP19900149409 19900607**Also published as:** EP0460279 (A3)
 EP0460279 (B1)
 US5319174 (A)
 JP4043591 (A)
 DE69015623 (T2)[more >>](#)**Cited documents:** GB2108786 (A)
 US4736082 (A)
 EP0092588 (A1)
 US4426564 (A)
 GB2135538 (A)**Abstract of EP 0460279 (A2)**

An induction heating cooker has a rectifier (13) for producing pulsating DC current, an inverter (14) which receives the pulsating current given from the rectifier (13) and produces a high frequency output current and a load circuit (15) including an output coil (21) and a resonance capacitor (22) connected in series to each other to receive high frequency output signal from the inverter, and the variation or adjustment of output electromagnetic power is made by changing duty ratio of the high frequency output current of the inverter by giving variable duty ratio setting signals to switching elements (Qa, Qb) in the inverter 14; the frequency of the high frequency output current of the inverter 14 is fixed, and therefore, unpleasant interference sound is not generated at parallel simultaneous operation.

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